



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nedovic et al.

For: **CONDITIONAL PRE-CHARGE
METHOD AND SYSTEM**

Serial No.: 10/043933

Filed: January 11, 2002

Atty. Docket No.: 25916-0238 (6136/60499)

) Examiner: Not Assigned

) Group Art Unit: Not Assigned

) Customer No.: 23422

) **INFORMATION DISCLOSURE
STATEMENT**

BOX PATENT APPLICATION

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Applicants wish to bring to the attention of the Patent Office the references listed on the attached form PTO-1449 and request that they be considered by the Examiner. Copies of the cited references are enclosed herewith.

This information disclosure statement is being filed under 37 C.F.R. §1.97(b), therefore, no fee is due. The Commissioner is authorized to charge any additional fees and to credit any overpayment of fees which may be required under 37 C.F.R. §1.16 and §1.17 to our Deposit Account No. 13-0201, referencing Atty. Docket No. 25916-238

Respectfully submitted,

By: _____

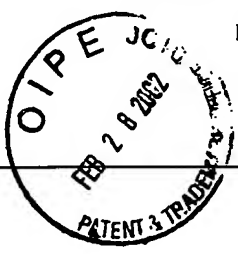
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CERTIFICATE OF MAILING PURSUANT TO 37 C.F.R. §1.8

I hereby certify that this paper is being deposited in the U.S. Postal Service as First Class mail, with sufficient postage addressed to BOX PATENT APPLICATION Commissioner for Patents, Washington D.C. 20231, on February 15, 2002, in Palo Alto, CA.

Shawn Anderson

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.56, §1.97, and §1.98 PTO-1449 FORM SHEET 1 OF 1				ATTORNEY DOCKET NO.: 025916-0238	SERIAL NO.: 10/043933	
				APPLICANTS: Nedovic et al.		
				FILING DATE: January 11, 2002	GROUP ART UNIT: Not Assigned	
U.S. PATENT DOCUMENTS						
† EX'R INITIAL	*REF. #	PATENT NUMBER	DATE (MO/YR)	NAME	U.S. CLASS/ SUBCLASS	FILING DATE (If appropriate)
	A1	US5764089	06/09/1998	H. Partovi et al.	327	08/30/1996
	A2	US5774005	6/30/1998	H. Partovi et al.	327	08/30/1996
	A3	US5898330	04/27/1999	E. F. Klass	327	06/3/1997
	A4	US5990717	11/23/1999	H. Partovi et al.	327	03/09/1998
	A5	US6087872	7/11/2000	H. Partovi et al.	327	2/23/1998
	A6	US5917355	06/29/1999	E. F. Klass	327	1/16/1997
FOREIGN PATENT DOCUMENTS						
† EX'R INITIAL	*REF. #	PATENT NUMBER	DATE (MO/YR)	COUNTRY	TRANSLATION (YES/NO)	
OTHER DOCUMENTS						
† EX'R INITIAL	*REF. #	CITATION (Author, Article Title, Journal/Book Title, Date, Pertinent Pages, etc.)				
	C1	H. Partovi et al., "Flow-Through Latch and Edge-Triggered Flip-Flop Hybrid Elements", 1996 IEEE International Solid-State Circuits Conference. Digest of Technical Papers, ISSCC, San Francisco, CA, USA, February 8-10, 1996.				
	C2	B.-S. Kong, S.-S Kim, Y.-H. Jun, "Conditional-Capture Flip-Flop Technique for Statistical Power Reduction" 2000 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, pp. 290-291, February 2000.				
	C3	F. Klass, "Semi-Dynamic and Dynamic Flip-Flops with Embedded Logic", Symp. on VLSI Circ. Digest of Technical Papers, June 1998.				
	C4	J. Yuan, C. Svensson, "High-Speed CMOS Circuit Technique", IEEE Journal of Solid-State Circuits, Vol. 24, (No.1), pp. 62-70, February 1, 1989.				
	C5	N. Nedovic, V. G. Oklobdzija, "Hybrid Latch Flip-Flop with Improved Power Efficiency", Proceedings of Symposium on Integrated Circuits and Systems Design, pp. 211-215, 2000.				
EXAMINER'S SIGNATURE				DATE CONSIDERED		
† EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609. Line through citation if not in conformance and not considered. <i>Include copy of this form in next communication to applicant.</i>						
* If an asterisk is placed beside the reference number, a copy is not provided because the reference was previously cited by or submitted to the PTO in a prior application that is identified in the statement and relied upon for an earlier filing date under 35 U.S.C. 120. 37 C.F.R. 1.98(d).						